In re Application of: Mitchell Reid and Time y J. Dupuis

Filed:

January 10, 2000

For:

INTEGRATED MODEM AND LINE-ISOLATION CIRCUITRY WITH

HDLC FRAMING AND ASSOCIATED METHOD

Serial No.:

09/480,747

Group Art Unit:

2634

Examiner:

Munoz, Guillermo

Atty Docket No.:

SILA:054

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Technology Center 2600

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

RESPONSE TO OFFICE ACTION MAILED SEPTEMBER 11, 2003

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted as a Response to the Office Action dated September 11, 2003. A Request for Extension of Time of one-month has been concurrently filed, which brings the response date to January 11, 2004.

Should any fees under 37 CFR 1.16-1.21 be required for any reason relating to the enclosed materials, including any additional fee for an extension of time, the Commissioner is authorized to deduct such fees from O'Keefe, Egan & Peterman Deposit Account No. 10-1205/SILA:054.